

● Circuit Diagram

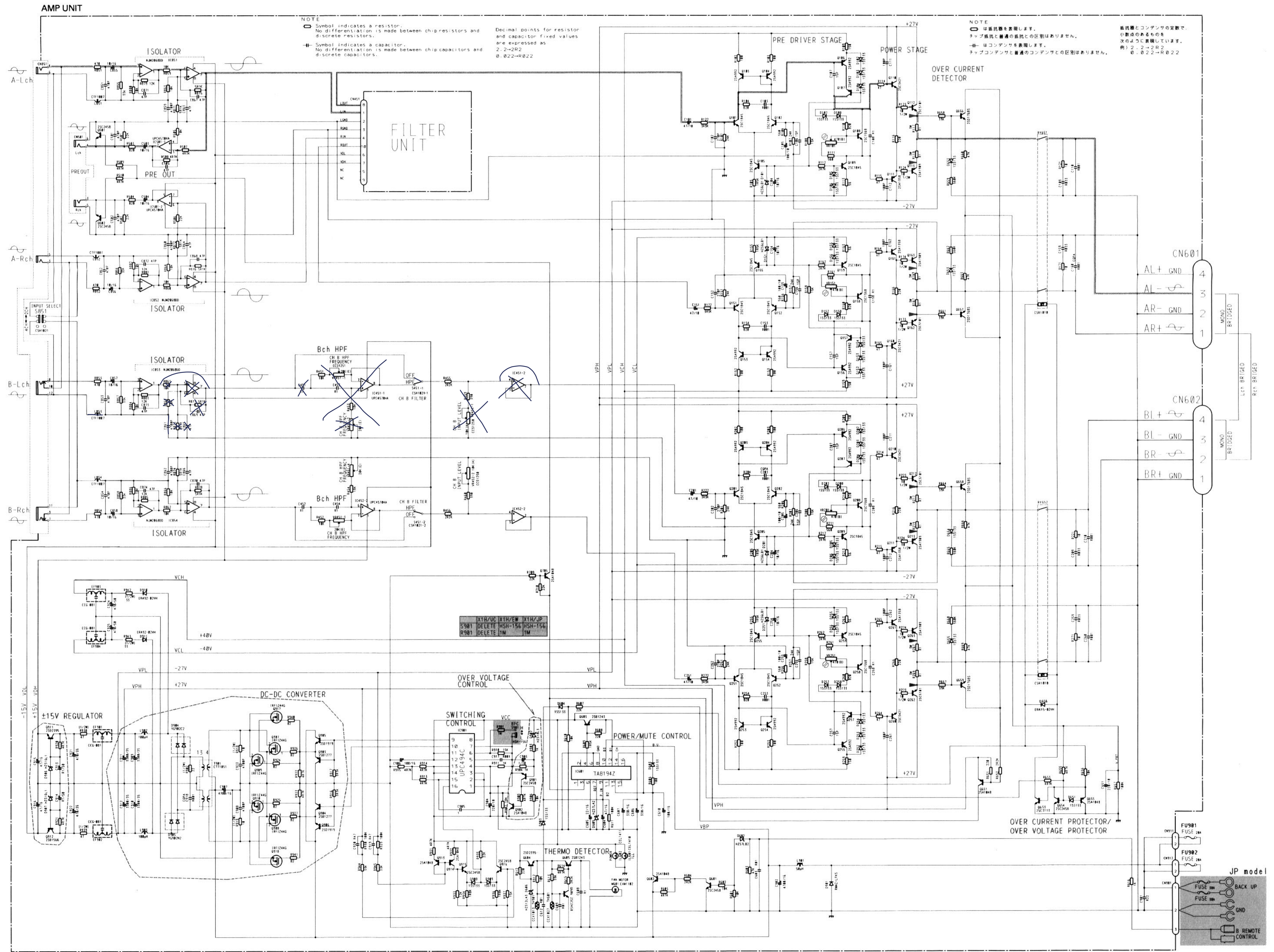


Fig.4